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APPLICANTS

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** CONTINUING DATA ***** none K.N.

** FOREIGN APPLICATIONS ***** yes K.N.
 JAPAN 2000-4296 01/13/2000

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Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR COUNTRY JAPAN	SHEETS DRAWING 6	TOTAL CLAIMS 13	INDEPENDENT CLAIMS 2
35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance				
Verified and Acknowledged <i>K. N. W.</i> Examiner's Signature Initials				

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TITLE

Semiconductor device and manufacturing method therefor

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